

Form PTO-1449 (modified)

List of Patents and Publications for Applicant's

Atty. Docket No. P3355US1 (119-0035US) Application No. 10/826,596

First Named Inventor/Applicant:
Mark Zimmer / Apple Computer, Inc.Examiner Name: Mia M. Thomas
Unknown

Filing Date: April 16, 2004 Group Art Unit: 262T 2624

O I P E
INFORMATION DISCLOSURE
STATEMENT

(Use several sheets if necessary)

Sheet

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of

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U.S. Patent Documents

Exam. Init.	Ref. Des.	Document Number	Date	Name	Class	Sub Class	Filing Date of App.
/MT/	A1	6,717,599	04/06/2004	Olano	345	853	06/29/2000
	A2	2003/174136-A1	09/18/2003	Emberling et al.			
	A3	2002/118217-A1	08/29/2002	Fujiki			
	A4	6,272,558	08/07/2001	Hui et al.	709	328	10/06/1997
	A5	6,006,231	12/21/1999	Popa	707	101	09/10/1997
▼	A6	5,490,246	02/06/1996	Brotsky et al.	395	161	08/23/1994

Foreign Patent Documents

Exam. Init.	Ref. Des.	Document Number	Date	Country	Class	Sub Class	Translation Yes/No
/MT/	B1	EP 1 383 080	01/21/2004	EP			
	B2	WO 02/09039A	01/31/2002	WO			
	B3	EP 0 694 879	01/31/1996	EP			
▼	B4	EP 548 586 A	06/30/1993	EP			

Other Art (Including Author, Title, Date Pertinent Pages, Etc.)

Exam. Init.	Ref. Des.	Citation
/MT/	C1	International Search report dated July 27, 2005 (PCT/US 05/008804; 119-0033WO)
	C2	International Search report dated August 8, 2005 (PCT/US 05/008805; 119-0034WO)
▼	C3	Haeberli, P. et al., "The Accumulation Buffer: Hardware Support for High-Quality Rendering," Computer Graphics, New York, NY, vol. 24, No. 4, Aug. 1, 1990, pgs. 309-318.

EXAMINER: /Mia Thomas/

DATE CONSIDERED: 05/08/2007

EXAMINER: INITIAL IF REFERENCE CONSIDERED, WHETHER OR NOT CITATION IS IN CONFORMANCE WITH MPEP609; DRAW LINE THROUGH CITATION IF NOT IN CONFORMANCE AND NOT CONSIDERED. INCLUDE COPY OF THIS FORM WITH NEXT COMMUNICATION TO APPLICANT.

Form PTO-1449 (modified)		Atty. Docket No.: 119-0035US	Serial No. 10/826,596
List of Patents and Publications for Applicant's <small>INFORMATION DISCLOSURE STATEMENT</small> <small>(Use several sheets if necessary)</small>		Applicant Mark Zimmer Title: Improved Blur Computation Algorithm Filing Date: April 16, 2004 Group: 2624 U.S. Patent Documents <i>See Page 1</i> Foreign Patent Documents <i>See Page 2</i> Other Art <i>See Page 3</i>	

U.S. Patent Documents

Exam. Init.	Ref. Des.	Document Number	Date	Name	Class	Sub Class	Filing Date of App.
/MT/	A1	6,580,430	06/17/2003	Hollis, et al.	345/	473	
	A2	5,949,409	09/07/1999	Tanaka, et al.	345	549	
	A3	6,211,890	04/03/2001	Ohba	345	506	
	A4	6,707,462	03/16/2004	Peercy, et al.	345/	619	
	A5	5,388,201	02/07/1995	Hourvitz, et al.	345	794	
	A6	6,489,963	12/03/2003	Parikh, et al.	345	522	
	A7	6,577,317	06/10/2003	Duluk, Jr., et al.	345	506	
	A8	6,466,218	10/15/2002	Parikh, et al.	345	522	
	A9	6,031,937	02/29/2000	Graffagnino	382	236	
	A10	2002/0093516A1	07/18/2002	Brunner, et al.	345	629	
	A11	6,166,748	12/26/2000	Van Hook, et al.	345	522	
	A12	2003/0123739A1	09/05/2002	Graffagnino	382	236	
	A13	6,526,174	02/25/2003	Graffagnino	382	236	
	A14	5,933,148	08/03/1999	Oka, et al.	345	427	
	A15	6,614,444	09/02/2003	Duluk, Jr., et al.	345	581	
	A16	6,664,958	12/16/2003	Leather, et al.	345	422	
	A17	6,452,600	09/17/2002	Parikh, et al.	345	522	
	A18	6,697,074	02/24/2004	Parikh, et al.	345	522	
	A19	6,571,328	05/27/2003	Liao, et al.	712	35	
	A20	6,246,418	06/12/2001	Oka	345	441	
	A21	6,369,823	04/09/2002	Ohba	345	506	
	A22	6,456,290	09/24/2002	Parikh, et al	345	522	
V	A23	6,664,962	12/16/2003	Komsthoeft , et al	345	426	

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10/826,596**List of Patents and Publications for Applicant's****INFORMATION DISCLOSURE STATEMENT**

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U.S. Patent Documents
*See Page 1*Foreign Patent Documents
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*See Page 3***U.S. Patent Documents**

Exam. Init.	Ref. Des.	Document Number	Date	Name	Class	Sub Class	Filing Date of App.
/MT/	A24	6,369,830	04/09/2002	Brunner, et al.	345	629	
	A25	6,639,595	10/28/2003	Drebin, et al.	345	426	
	A26	6,411,301	06/25/2002	Parikh, et al.	345	522	
	A27	5,793,376	08/11/1998	Tanaka, et al.	345	582	
	A28	6,636,214	10/21/2003	Leather, et al.	345	422	
	A29	6,421,058	07/16/2002	Parikh, et al	345	522	
	A30	2002/0174181A1	11/21/2002	Wei	709	204	
	A31	6,618,048	09/09/2003	Leather	345	422	
	A32	6,609,977	08/26/2003	Shimizu, et al.	463	36	
	A33	6,424,348	07/23/2002	Parikh	345	522	
	A34	6,075,543	06/13/2000	Akeley	345	539	
	A35						
	A36						
V	A37						

Foreign Patent Documents

Exam. Init.	Ref. Des.	Document Number	Date	Country	Class	Sub Class	Translation Yes/No
/MT/	B1	WO 98/45815	10/15/1998	WIPO	G06T	15/70	Yes
/MT/	B2	WO 2004-027707A2	04/01/2004	WIPO	G06T	1/00	Yes (Abstract Only)
/MT/	B3	EP 0 972 273 B1	24.03.2004	EPO	G06T	15/70	Yes
	B4						
	B5						

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10/826,596**List of Patents and Publications for Applicant's****INFORMATION DISCLOSURE STATEMENT**

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Applicant

Mark ZimmerTitle: **Improved Blur Computation Algorithm**Filing Date:
April 16, 2004Group:
3624 2624

U.S. Patent Documents

See Page 1

Foreign Patent Documents

See Page 2

Other Art

See Page 3**Other Art (Including Author, Title, Date Pertinent Pages, Etc.)**

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/MT/	C1	nVIDIA, "Cg – Teaching Cg" Power Point Presentation, Author and date unknown.
	C2	Shantzis, "A Model for Efficient and Flexible Image Computing" Computer Graphics Proceedings, Annual Conference Series, 1994, pp. 147 – 154.
	C3	Akeley, et al., "Real-Time Graphics Architecture" http://www.graphics.stanford.edu/courses/cs448a-01-fall , The OpenGL® Graphics System – CS448 Lecture 15, Fall 2001, pp. 1 – 20.
	C4	Gelder, et al., "Direct Volume Rendering with Shading via Three-Dimensional Textures" Computer Science Dept., Univ. of California, Santa Cruz, CA 95064.
	C5	Elliott, "Programming Graphics Processors Functionally,"
	C6	Segal, et al., "The OpenGL® Graphics System: A Specification (Version 1.5)" Copyright © 1992-2003 Silicon Graphics, Inc., October 30, 2003
V	C7	

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